	Type	L #	Hits	Search Text	DBs	Time Stamp
3	BRS	L3	14166	(fpga pla pld asic (programmable adj logic adj (device array))) and (column and row)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/28 15:20
4	BRS	L4	10341	3 and input and output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/28 15:20
5	BRS	L5	272	4 and die same side	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2004/12/28 15:22